What is claimed is:

1.   1.   A method comprising:

2.   receiving an indication of a thermal event in a processor, the processor being part of a

3.   computer system;

4.   in response to the indication, powering down the processor; and

5.   subsequent to the powering down of the processor, powering down other components

6.   of the computer.

1.   2.   The method of claim 1, wherein said other components are located on a

2.   motherboard of the computer system.

1.   3.   The method of claim 1, further comprising:

2.   introducing a predetermined delay after the receiving before said powering down

3.   other components of the computer.

1.   4.   The method of claim 1, wherein said power down other components

2.   comprises:

3.   controlling a state of a signal indicative of a mechanical power switch of the computer

4.   system.

1.   5.   The method of claim 1, wherein said powering down the processor comprises:

2.   cutting off a supply voltage to the processor.

1.   6.   The method of claim 1, wherein said powering down other components

2.   comprises:

3.   cutting off at least one supply voltage to said other components.

1   7.   A computer system comprising:

2   a processor capable of indicating a thermal event;

3   power consuming components;

4   a power supply subsystem to supply power to the processor and power consuming

5 components; and

6   a circuit to:

7   receive an indication of a thermal event in the processor, and

8   in response to the indication, cause the power supply subsystem to power

9 down the processor before powering down the power consuming components.

1   8.   The computer system of claim 7, wherein said power consuming components

2 are located on a motherboard of the computer system.

1   9.   The computer system of claim 7, wherein the computer system introduces a

2 delay in power down said power consuming components.

1   10.   The computer system of claim 7, further comprising:

2   a mechanical switch to turn power to the computer system on and off, the computer

3 system having a signal indicative of a state of the switch, wherein

4   the circuit controls the signal to cause the power down of said power consuming

5 components.

1   11.   The computer system of claim 7, wherein the power supply subsystem powers

2 down the processor by cutting off a supply voltage to the processor.

1   12.   The computer system of claim 7, wherein the power supply subsystem powers

2 down the power consuming components by cutting off at least one supply voltage to said

3 other components.

1   13.   A method comprising:

2   receiving an indication of a thermal event in a processor, the processor being part of a

3 computer system;

4   in response to the indication, introducing a delay;

5   in response to the indication, powering down the processor before the expiration of

6 the delay; and

7   powering down powering down other components of the computer in response to the

8 expiration of the delay.

1   14.   The method of claim 13, wherein said other components are located on a

2 motherboard of the computer system.

1   15.   The method of claim 13, wherein said powering down other components

2 comprises:

3   controlling a state of a signal indicative of a mechanical power switch of the computer

4 system.

1   16.   The method of claim 13, wherein said powering down the processor

2 comprises:

3   cutting off a supply voltage to the processor.

1   17.   The method of claim 13, wherein said powering down other components

2 comprises:

3   cutting off at least one supply voltage to said other components.